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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**SUBMISSION OF FORMAL DRAWINGS**


APPLICANT: Albrecht Mayer ALLOWED: 12/29/2005  
SERIAL NO.: 09/787,869 09/530549 GROUP ART UNIT: 2123  
FILED: June 30, 2000 EXAMINER: Dwain M. Craig  
INVENTION: "Method and Device for System CONFIRMATION NO.: 8828  
Simulation of Microcontrollers/Microprocessors  
and Corresponding Peripheral Modules"

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

S I R:

Applicant herewith submits 2 sheets of Formal Drawings (Figs. 1 and 2) for the above application. The drawings have been revised to include the changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached to the Notice of Allowability.

Submitted by,

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450 on January 17, 2006.

